

University of Mumbai



1T01116

SECOND HALF of (Winter) 2018

EXAMINATION TIME TABLE
PROGRAMME - T.E. (Electronics) (REV. -2012)(CBSGS)
SEMESTER - VI

Days and Dates	Time	Paper Code	Paper
Friday, November 16, 2018	02:30 p.m. to 05:30 p.m.	37201	BASIC VLSI DESIGN
Monday, November 26, 2018	02:30 p.m. to 05:30 p.m.	37202	ADVANCED INSTRUMENTATION SYSTEM
Friday, November 30, 2018	02:30 p.m. to 05:30 p.m.	37203	COMPUTER ORGANIZATION
Friday, December 7, 2018	02:30 p.m. to 05:30 p.m.	37204	POWER ELECTRONICS - I
Thursday, December 13, 2018	02:30 p.m. to 05:30 p.m.	37205	DIGITAL SIGNAL PROCESSING AND PROCESSORS
Wednesday, December 19, 2018	02:30 p.m. to 04:30 p.m.	37206	MODERN INFORMATION TECHNOLOGY FOR MANAGEMENT

Important Note: •The candidates appearing for the examination should report 20 minutes before the start of examination.

- Mobile phones and other electronic gazets are prohibited in the examination hall.
- Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098
24th September, 2018.

ST-166

Sd/-
(Prof. (Dr.) Arjun Ghatule)
Director
Board of Examinations & Evaluation